

**IN THE CLAIMS:**

Claims 4, 10, and 20 have been amended herein. Please note that all claims currently pending and under consideration in the referenced application are shown below. Please enter these claims as amended. This listing of claims will replace all prior versions and listings of claims in the application.

**Listing of Claims:**

Claims 1-3 (canceled)

4. (currently twice amended) ~~A semiconductor device according to claim 2, wherein the openings comprise~~ A semiconductor device having an improved bond pad, the semiconductor device comprising:

- a. a bond pad electrically connected to an active circuit in the semiconductor device;
- b. a substantially flat bonding surface on the bond pad; and
- c. a plurality of openings extending partially into the bonding surface and forming a pattern of radiating channels disposed about a center of the bonding surface.

Claims 5-9 (canceled)

10. (currently amended) ~~A semiconductor device according to claim 8, A semiconductor device, which comprises:~~

- a. an active circuit in the semiconductor device;
- b. a wiring pattern overlying and in electrical contact with the active circuit;
- c. bond pads formed as select areas on the wiring pattern; and
- d. a plurality of openings extending partially into a substantially flat bonding surface of the bond pads, wherein the openings comprise a pattern of radiating channels disposed about a center of the bonding surface.

Claims 11-19 (canceled)

20. (currently amended) An improved bond pad according to claim 18 An improved bond pad comprising:

a metal layer having a substantially planar surface, said metal layer electrically connected to an active circuit of a semiconductor device and having a plurality of openings extending partially into said metal layer, wherein the openings comprise a pattern of radiating channels disposed about a center of the bond pad.

Claims 21-30 (canceled)